

Part Number: **4171700XX-3**

Model: **SVH-1920+**

DATE	VERSION	SUMMARY OF CHANGES	NOTE
16 October 2008	10	Release pilot run to production Bare PCB: 017170001-3; P/N 417170010-3 BIOS: U25, E 0.01.00 (Flash SST39VF3201) U32, V0.40 (ARGB DDC, 24C02) U19, V0.60 (DVI DDC, 24C02)	ECN# 870689023
29 July 2009	10 to 11	To enable HDCP and DVI/HDMI recognition(see rework instruction) - Cut trace between R8 and R9 - Solder a wire wrap wire between R9(near the cut end) and pin6 of U32 To standardize the OSD switch mount interface hardware as SVH-1920 - Remove R282 and R283 - Solder 2pcs 1% 10K resistor between CNC1's pin8 & 9 and CNC1's pin10 & 11. To increase 1.8V power stability of FLI30436 - Solder a AWG22 wire between FB10 & FB12 Firmware on U25 upgrade to E0.16.00 Bare PCB: 017170001-3; P/N 417170011-3 BIOS: U25, E 0.16.00 (Flash SST39VF3201)(changed) U32, V0.50 (ARGB DDC, 24C02)(changed) U19, V0.70 (DVI DDC, 24C02)(changed)	ECN# 870689097
19 April 2010	11 to 12	Official release firmware on U25, V0.27.00 Hardware change - Correct 8pcs 0.1uF/10V capacitor(C47, C162, DC1, DC32, DC127, DC128, DC130 & DC133) to voltage rating 35V to prevent it may burn out when using 12V/24V input Bare PCB: 017170001-3; P/N 417170012-3 BIOS: U25, V 0.27.00 (Flash SST39VF3201)(changed) U32, V0.60 (DVI DDC, 24C02)(changed) U19, V0.70 (ARGB DDC, 24C02)(no change)	ECN# 870689156
21 April 2010	1x to 20	Hardware change - Use new PCB to fix rework on previous versions Bare PCB: 017170002-3; P/N 417170020-3 BIOS: U25, V 0.27.00 (Flash SST39VF3201)(no change) U32, V0.60 (DVI DDC, 24C02)(no change) U19, V0.70 (ARGB DDC, 24C02)(no change)	ECN# 870689157

<p>26 October 2010</p>	<p>20 to 21</p>	<p>Upgrade firmware to V0.35.00.00</p> <p>Feature change :</p> <ol style="list-style-type: none"> 1. Change PIP Size range to 1~18 instead of Small/Medium/Large. 2. a. JC jumper JC1-4 that will enable or disable the display of this menu's lines 1 & 2 ("Reset Factory Defaults", Reset factory defaults with Color Temp). <ol style="list-style-type: none"> b. Rename line 3 to "Save Current Settings as Calibrated Values. c. Rename line 4 to "Recall Stored Calibrated Values". d. Added a Double Confirm message 1-4: Are you sure? Yes, No e. Added "Return" on Defaults Setting menu 3. Added the new item on Save Current Settings as Calibrated Values function <ol style="list-style-type: none"> a. Backlight Value b. B/L Invert c. B/L Control d. Backlight Frequency e. Light Detector f. AutoSourceSelect Port Enable Setting 4. Added 1680x1050 standard timing on DVI DDC 5. When PIP sets Side by Side under no video input state, display the searching menu on main source after auto source seek 6. Added RS-232 command PIP source select "0xA7", when Query feedback 0x40 0x30 = PIP off 7. Lock the "Backlight" setting to 1 for AU 65" P645HW01 panel. 8. Increase the detection time on DVI port (~ 10s), when auto source seek set "ON" 9. When PIP source is stable and main source is no signal, the panel will not go to power down mode. <p>Panel support :</p> <ol style="list-style-type: none"> 12. Added Samsung LTI430LA01 (1920x480) panel support [SW3 Pos 1~7 : Off Off On Off On On On]. 13. Added the NEC_NL10260BC1901D (1024x600) panel support [SW1-7 Pos 1~7 : Off On On Off On On On] <p>Bug fixed :</p> <ol style="list-style-type: none"> 14. Fixed DVI signal detect error "Lost half horizontal resolution" 15. Correct OSD Backlight Setting display error problem 16. Fix noise and tearing when the marker menu is displayed in 16:9 mode of 1080i. 17. Fix PIP Lost Position 18. Aspect/Size 4:3 lost green dot 19. Fix soft power command response for composite and VGA are too long approx 15-20 secs. 20. Fix the PIP_Swap Hotkey no function at main source is "no signal". 21. Fix Soft power up, PIP window not display problem when "PIP source is no signal" <p>Bare PCB: 017170002-3; P/N 417170021-3 BIOS: U25, V 0.35.00.00 (Flash SST39VF3201)(changed) U32, V0.70 (DVI DDC, 24C02)(changed) U19, V0.70 (ARGB DDC, 24C02)(no change)</p>	<p>ECN# 870689205</p>
<p>16 April 2012</p>	<p>21 to 22</p>	<p>Hardware change</p> <ul style="list-style-type: none"> - to improve stability of 1.8V for the main video processor FLI30436 by changing C117 and C108 from 1uF to 100uF/6V3. <p>Bare PCB: 017170002-3; P/N 417170022-3 BIOS: U25, V 0.35.00.00 (Flash SST39VF3201)(no change) U32, V0.80 (DVI DDC, 24C02)(no change) U19, V0.70 (ARGB DDC, 24C02)(no change)</p>	<p>ECN# 870689295</p>

3 January 2013	22 to 23	<p>Hardware no change Firmware changes</p> <ol style="list-style-type: none"> 1) Cover all feature changes and bugs fixed up to V0.45.00.01 from SVX-1920(General). 2) Add panel support : <ul style="list-style-type: none"> Support AU Optronics M200RW01 V1 (1600x900) panel (SW3 1~7 : ON ON ON OFF ON ON ON) Support AU Optronics P645HW03 V0 (1920x1080) panel (SW3 Pos 1~7 : Off Off On On Off Off Off) Support NEC NL10260BC19-01D (1024x600) panel (SW3 Pos 1~7 : Off On On Off On On On) Support Sharp_LQ070Y3LG4A (800x480) panel. (SW3 Pos 1-7 : On Off On Off On On On) Support Samsung LTM201M1-L01 (1680x1050) panel. SW3 1-7 : On Off Off On On On On 3) Set 1920x1080 as preferred and still has 1920x1200 as the max on EDID contents. 4) Update EDID file content to improve the compatibility for some particular latest graphics display card (Set "16:9" aspect ratio by changing the image size parameter in the ARGB, DVI & HDMI EDID content). 5) Shorten the detection time on HDMI and DVI port (~ 10s), when auto source seek set "ON" 6) Fixed the port switching error on HD-2000 (e.g Port 1 > Port 2 or Port 2 > Port 1) 7) Add hotkey function to control turning PIP on and off on OSD menu & RS-232 command. 8) Incorporate the IR display addressing capability. 9) Remove the WSS/AFD, and UMD function out. <p>Bare PCB: 017170002-3; P/N 417170023-3</p> <p>BIOS: U25, V 0.50.00.00 (Flash SST39VF3201)(no change) U32, V0.90 (DVI DDC, 24C02)(no change) U19, V0.80 (ARGB DDC, 24C02)(no change)</p>	ECN# 870689347
Prepared by: DV-HK		Date: 3 Jan 2013	